



K24U 0832

Reg. No. : .....

Name : .....

IV Semester B.C.A. Degree (CBCSS – OBE – Regular/Supplementary/  
Improvement) Examination, April 2024

(2019 to 2022 Admissions)

Core Course

4B09BCA : COMPUTER ORGANIZATION

Time : 3 Hours

Max. Marks : 40

PART – A  
(Short Answer)

Answer **all** questions.

(6×1=6)

1. What is the purpose of control memory address register ?
2. What is immediate addressing mode ?
3. What is the primary focus of CISC processors ?
4. What are the main components involved in the strobe method of data transfer ?
5. What is meant by cache hit rate ?
6. What is page replacement ?

PART – B  
(Short Essay)

Answer **any six** questions.

(6×2=12)

7. Write a short note on micro programmed control.
8. Explain the phases of instruction cycle in the computer organization.
9. Define the purpose of various buses in computer architecture.
10. What are the features of the hardwired control ?

P.T.O.



11. Explain instruction set architecture and its components.
12. What is meant by locality of reference ?
13. Differentiate between synchronous and asynchronous bus.
14. Write a short note on :
  - a) Magnetic disk drive
  - b) Optical drives.

**PART - C**  
**(Essay)**

Answer **any four** questions.

**(4×3=12)**

15. Compare and contrast between DMA and I/O processors.
16. Discuss the strobe method of data transfer, highlight its key principles.
17. Explain various addressing modes.
18. What is meant by virtual memory management ?
19. List the characteristics of HISC architecture.
20. Explain hardware interrupts in detail.

**PART - D**  
**(Long Essay)**

Answer **any two** questions.

**(2×5=10)**

21. Explain the various registers used in a computer.
  22. Describe asynchronous data transfer in detail.
  23. Differentiate between SRAM and DRAM.
  24. Explain the different modes of I/O data transfer.
-